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Quickstart Manual OSELAS.BSP() FriendlyARM mini2440



Pengutronix e. K.
Peiner Straße 6-8
31137 Hildesheim

+49 (0)51 21 / 20 69 17 - 0 (Fon)
+49 (0)51 21 / 20 69 17 - 55 55 (Fax)

info@pengutronix.de



Cut Here and Stick on your Monitor

Don't Panic

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Part I

OSELAS Quickstart for FriendlyARM mini2440

1 You have been warned

The barebox bootloader and the Linux kernel contained in this board support package will modify your NAND memory. This is important to know if you want to keep a way back to the previous usage. At least the bad block marker maybe lost if you try to switch back to the old behaviour.

One word about using NAND memory for the bootloader and the filesystem:

NAND memory can be forgetful. That is why some kind of redundancy information is always required. This board support package uses ECC (error-correcting code) checksums as redundancy information when the bootloader and the Linux kernel are up and running.

This kind of redundancy information can repair one bit errors and detect two bit errors in a page of data. Its very important to use ECC at least for the bootloader to ensure to bring up the Mini2440 successfully. But its currently not done in the bootloader while bootstrapping. So, there is still a risk for long term use to fail booting the Mini2440 from NAND. In this case the bootloader must be re-written making the Mini2440 booting again from NAND.

In one of the next releases, ECC check and correction will be done while bootstrapping as well, to make the system more reliable for long term use. But then one question will be still open: Does the hardware of the S3C2440 CPU ECC check and correction for the very first page? I guess no, because the hardware has no idea, where the ECC checksum is stored. So, maybe there is no 100 % reliable solution for long term users.

2 Getting a working Environment

2.1 Download Software Components

In order to follow this manual, some software archives are needed. There are several possibilities how to get these: either as part of an evaluation board package or by downloading them from the Pengutronix web site.

The central place for OSELAS related documentation is <http://www.oselas.com>. This website provides all required packages and documentation (at least for software components which are available to the public).

To build OSELAS.BSP-Pengutronix-Mini2440-2011.05.0, the following archives have to be available on the development host:

- `ptxdist-2011.05.0.tar.bz2`
- `OSELAS.BSP-Pengutronix-Mini2440-2011.05.0.tar.gz`
- `OSELAS.Toolchain-2011.03.0.tar.bz2`

If they are not available on the development system yet, it is necessary to get them.

2.2 PTXdist Installation

The PTXdist build system can be used to create a root filesystem for embedded Linux devices. In order to start development with PTXdist it is necessary to install the software on the development system.

This chapter provides information about how to install and configure PTXdist on the development host.

2.2.1 Main Parts of PTXdist

The most important software component which is necessary to build an OSELAS.BSP() board support package is the `ptxdist` tool. So before starting any work we'll have to install PTXdist on the development host.

PTXdist consists of the following parts:

The `ptxdist` Program: `ptxdist` is installed on the development host during the installation process. `ptxdist` is called to trigger any action, like building a software packet, cleaning up the tree etc. Usually the `ptxdist` program is used in a *workspace* directory, which contains all project relevant files.

A Configuration System: The config system is used to customize a *configuration*, which contains information about which packages have to be built and which options are selected.

Patches: Due to the fact that some upstream packages are not bug free – especially with regard to cross compilation – it is often necessary to patch the original software. PTXdist contains a mechanism to automatically apply patches to packages. The patches are bundled into a separate archive. Nevertheless, they are necessary to build a working system.

Package Descriptions: For each software component there is a “recipe” file, specifying which actions have to be done to prepare and compile the software. Additionally, packages contain their configuration snippet for the config system.

Toolchains: PTXdist does not come with a pre-built binary toolchain. Nevertheless, PTXdist itself is able to build toolchains, which are provided by the OSELAS.Toolchain() project. More in-deep information about the OSELAS.Toolchain() project can be found here: http://www.pengutronix.de/oselas/toolchain/index_en.html

Board Support Package This is an optional component, mostly shipped aside with a piece of hardware. There are various BSP available, some are generic, some are intended for a specific hardware.

2.2.2 Extracting the Sources

To install PTXdist, at least two archives have to be extracted:

ptxdist-2011.05.0.tar.bz2 The PTXdist software itself

ptxdist-2011.05.0-projects.tgz Generic projects (optional), can be used as a starting point for self-built projects.

The PTXdist packet has to be extracted into some temporary directory in order to be built before the installation, for example the `local/` directory in the user's home. If this directory does not exist, we have to create it and change into it:

```
$ cd
$ mkdir local
$ cd local
```

Next step is to extract the archive:

```
$ tar -xjf ptxdist-2011.05.0.tar.bz2
```

and if required the generic projects:

```
$ tar -xzf ptxdist-2011.05.0-projects.tgz
```

If everything goes well, we now have a `PTXdist-2011.05.0` directory, so we can change into it:

```
$ cd ptxdist-2011.05.0
$ ls -lF
total 532
-rw-r--r-- 1 jb users 18361 Jan 6 14:50 COPYING
-rw-r--r-- 1 jb users 3865 Jan 6 14:50 CREDITS
-rw-r--r-- 1 jb users 115540 Jan 6 14:50 ChangeLog
-rw-r--r-- 1 jb users 57 Jan 6 14:50 INSTALL
-rw-r--r-- 1 jb users 2228 Jan 6 14:50 Makefile.in
-rw-r--r-- 1 jb users 4196 Jan 6 14:50 README
-rw-r--r-- 1 jb users 691 Jan 6 14:50 REVISION_POLICY
-rw-r--r-- 1 jb users 63019 Jan 6 14:50 TODO
-rwxr-xr-x 1 jb users 28 Jan 6 14:50 autogen.sh
drwxr-xr-x 2 jb users 4096 Jan 6 14:50 bin
drwxr-xr-x 9 jb users 4096 Jan 6 14:50 config
-rwxr-xr-x 1 jb users 213205 Jan 6 16:29 configure
-rw-r--r-- 1 jb users 12539 Jan 6 14:50 configure.ac
drwxr-xr-x 10 jb users 4096 Jan 6 14:50 generic
```



```
drwxr-xr-x 162 jb users 4096 Jan 6 14:50 patches
drwxr-xr-x 2 jb users 4096 Jan 6 14:50 platforms
drwxr-xr-x 4 jb users 4096 Jan 6 14:50 plugins
drwxr-xr-x 6 jb users 32768 Jan 6 14:50 rules
drwxr-xr-x 8 jb users 4096 Jan 6 14:50 scripts
drwxr-xr-x 2 jb users 4096 Jan 6 14:50 tests
```

2.2.3 Prerequisites

Before PTXdist can be installed it has to be checked if all necessary programs are installed on the development host. The configure script will stop if it discovers that something is missing.

The PTXdist installation is based on GNU autotools, so the first thing to be done now is to configure the packet:

```
$ ./configure
```

This will check your system for required components PTXdist relies on. If all required components are found the output ends with:

```
[...]
checking whether /usr/bin/patch will work... yes

configure: creating ./config.status
config.status: creating Makefile
config.status: creating scripts/ptxdist_version.sh
config.status: creating rules/ptxdist-version.in

ptxdist version 2011.05.0 configured.
Using '/usr/local' for installation prefix.

Report bugs to ptxdist@pengutronix.de
```

Without further arguments PTXdist is configured to be installed into `/usr/local`, which is the standard location for user installed programs. To change the installation path to anything non-standard, we use the `--prefix` argument to the `configure` script. The `--help` option offers more information about what else can be changed for the installation process.

The installation paths are configured in a way that several PTXdist versions can be installed in parallel. So if an old version of PTXdist is already installed there is no need to remove it.

One of the most important tasks for the `configure` script is to find out if all the programs PTXdist depends on are already present on the development host. The script will stop with an error message in case something is missing. If this happens, the missing tools have to be installed from the distribution before re-running the `configure` script.

When the `configure` script is finished successfully, we can now run

```
$ make
```

All program parts are being compiled, and if there are no errors we can now install PTXdist into its final location. In order to write to `/usr/local`, this step has to be performed as user `root`:

```
$ sudo make install
[enter password]
[...]
```

If we don't have root access to the machine it is also possible to install PTXdist into some other directory with the `--prefix` option. We need to take care that the `bin/` directory below the new installation dir is added to our `$PATH` environment variable (for example by exporting it in `~/.bashrc`).

The installation is now done, so the temporary folder may now be removed:

```
$ cd ../../
$ rm -fr local
```

2.2.4 Configuring PTXdist

When using PTXdist for the first time, some setup properties have to be configured. Two settings are the most important ones: Where to store the source packages and if a proxy must be used to gain access to the world wide web.

Run PTXdist's setup:

```
$ ptxdist setup
```

Due to PTXdist is working with sources only, it needs various source archives from the world wide web. If these archives are not present on our host, PTXdist starts the `wget` command to download them on demand.

Proxy Setup

To do so, an internet access is required. If this access is managed by a proxy `wget` command must be adviced to use it. PTXdist can be configured to advice the `wget` command automatically: Navigate to entry *Proxies* and enter the required addresses and ports to access the proxy in the form:

`<protocol>://<address>:<port>`

Source Archive Location

Whenever PTXdist downloads source archives it stores these archives in a project local manner. If we are working with more than one project, every project would download its own required archives. To share all source archives between all projects PTXdist can be configured to use only one archive directory for all projects it handles: Navigate to menu entry *Source Directory* and enter the path to the directory where PTXdist should store archives to share between projects.

Generic Project Location

If we already installed the generic projects we should also configure PTXdist to know this location. If we already did so, we can use the command `ptxdist projects` to get a list of available projects and `ptxdist clone` to get a local working copy of a shared generic project.

Navigate to menu entry *Project Searchpath* and enter the path to projects that can be used in such a way. Here we can configure more than one path, each part can be delimited by a colon. For example for PTXdist's generic projects and our own previous projects like this:

```
/usr/local/lib/ptxdist-2011.05.0/projects:/office/my_projects/ptxdist
```

Leave the menu and store the configuration. PTXdist is now ready for use.

2.3 Toolchains

Before we can start building our first userland we need a cross toolchain. On Linux, toolchains are no monolithic beasts. Most parts of what we need to cross compile code for the embedded target comes from the *GNU Compiler Collection*, `gcc`. The `gcc` packet includes the compiler frontend, `gcc`, plus several backend tools (`cc1`, `g++`, `ld` etc.) which actually perform the different stages of the compile process. `gcc` does not contain the assembler, so we also need the *GNU Binutils package* which provides lowlevel stuff.

Cross compilers and tools are usually named like the corresponding host tool, but with a prefix – the *GNU target*. For example, the cross compilers for ARM and powerpc may look like

- `arm-softfloat-linux-gnu-gcc`
- `powerpc-unknown-linux-gnu-gcc`

With these compiler frontends we can convert e.g. a C program into binary code for specific machines. So for example if a C program is to be compiled natively, it works like this:

```
$ gcc test.c -o test
```

To build the same binary for the ARM architecture we have to use the cross compiler instead of the native one:

```
$ arm-softfloat-linux-gnu-gcc test.c -o test
```

Also part of what we consider to be the "toolchain" is the runtime library (`libc`, dynamic linker). All programs running on the embedded system are linked against the `libc`, which also offers the interface from user space functions to the kernel.

The compiler and `libc` are very tightly coupled components: the second stage compiler, which is used to build normal user space code, is being built against the `libc` itself. For example, if the target does not contain a hardware floating point unit, but the toolchain generates floating point code, it will fail. This is also the case when the toolchain builds code for i686 CPUs, whereas the target is i586.

So in order to make things working consistently it is necessary that the runtime `libc` is identical with the `libc` the compiler was built against.

PTXdist doesn't contain a pre-built binary toolchain. Remember that it's not a distribution but a development tool. But it can be used to build a toolchain for our target. Building the toolchain usually has only to be done once. It may be a good idea to do that over night, because it may take several hours, depending on the target architecture and development host power.

2.3.1 Using Existing Toolchains

If a toolchain is already installed which is known to be working, the toolchain building step with PTXdist may be omitted.



The `OSELAS.BoardSupport()` Packages shipped for PTXdist have been tested with the `OSELAS.Toolchains()` built with the same PTXdist version. So if an external toolchain is being used which isn't known to be stable, a target may fail. Note that not all compiler versions and combinations work properly in a cross environment.

Every `OSELAS.BoardSupport()` Package checks for its `OSELAS.Toolchain` it's tested against, so using a different toolchain vendor requires an additional step:

Open the `OSELAS.BoardSupport()` Package menu with:

```
$ ptxdist platformconfig
```

and navigate to architecture ---> toolchain and check for specific toolchain vendor. Clear this entry to disable the toolchain vendor check.

Preconditions an external toolchain must meet:

- it shall be built with the configure option `--with-sysroot` pointing to its own C libraries.
- it should not support the *multilib* feature as this may confuse PTXdist which libraries are to select for the root filesystem

If we want to check if our toolchain was built with the `--with-sysroot` option, we just run this simple command:

```
$ mytoolchain-gcc -v 2>&1 | grep with-sysroot
```

If this command **does not** output anything, this toolchain was not built with the `--with-sysroot` option and cannot be used with PTXdist.

2.3.2 Building a Toolchain

PTXdist handles toolchain building as a simple project, like all other projects, too. So we can download the OSELAS.Toolchain bundle and build the required toolchain for the OSELAS.BoardSupport() Package.

A PTXdist project generally allows to build into some project defined directory; all OSELAS.Toolchain projects that come with PTXdist are configured to use the standard installation paths mentioned below.

All OSELAS.Toolchain projects install their result into `/opt/OSELAS.Toolchain-2011.03/`.



Usually the `/opt` directory is not world writeable. So in order to build our OSELAS.Toolchain into that directory we need to use a root account to change the permissions. PTXdist detects this case and asks if we want to run `sudo` to do the job for us. Alternatively we can enter:

```
mkdir /opt/OSELAS.Toolchain-2011.03
chown <username> /opt/OSELAS.Toolchain-2011.03
chmod a+rw /opt/OSELAS.Toolchain-2011.03.
```

We recommend to keep this installation path as PTXdist expects the toolchains at `/opt`. Whenever we go to select a platform in a project, PTXdist tries to find the right toolchain from data read from the platform configuration settings and a toolchain at `/opt` that matches to these settings. But that's for our convenience only. If we decide to install the toolchains at a different location, we still can use the *toolchain* parameter to define the toolchain to be used on a per project base.

2.3.3 Building the OSELAS.Toolchain for OSELAS.BSP-Pengutronix-Mini2440-2011.05.0

To compile and install an OSELAS.Toolchain we have to extract the OSELAS.Toolchain archive, change into the new folder, configure the compiler in question and start the build.

The required compiler to build the OSELAS.BSP-Pengutronix-Mini2440-2011.05.0 board support package is

```
arm-v4t-linux-gnueabi_gcc-4.5.2_glibc-2.13_binutils-2.21_kernel-2.6.36-sanitized
```

So the steps to build this toolchain are:



In order to build any of the OSELAS.Toolchains, the host must provide the tool *fakeroot*. Otherwise the message `bash: fakeroot: command not found` will occur and the build stops.

```
$ tar xf OSELAS.Toolchain-2011.03.0.tar.bz2
$ cd OSELAS.Toolchain-2011.03.0
$ ptxdist select ptxconfigs/└─
    arm-v4t-linux-gnueabi_gcc-4.5.2_glibc-2.13_binutils-2.21_kernel-2.6.36-sanitized.ptxconfig
$ ptxdist go
```

At this stage we have to go to our boss and tell him that it's probably time to go home for the day. Even on reasonably fast machines the time to build an OSELAS.Toolchain is something like around 30 minutes up to a few hours.

Measured times on different machines:

- Single Pentium 2.5 GHz, 2 GiB RAM: about 2 hours
- Turion ML-34, 2 GiB RAM: about 1 hour 30 minutes
- Dual Athlon 2.1 GHz, 2 GiB RAM: about 1 hour 20 minutes
- Dual Quad-Core-Pentium 1.8 GHz, 8 GiB RAM: about 25 minutes

Another possibility is to read the next chapters of this manual, to find out how to start a new project.

When the OSELAS.Toolchain project build is finished, PTXdist is ready for prime time and we can continue with our first project.

2.3.4 Protecting the Toolchain

All toolchain components are built with regular user permissions. In order to avoid accidental changes in the toolchain, the files should be set to read-only permissions after the installation has finished successfully. It is also possible to set the file ownership to root. This is an important step for reliability, so it is highly recommended.

2.3.5 Building Additional Toolchains

The OSELAS.Toolchain-2011.03.0 bundle comes with various predefined toolchains. Refer the `ptxconfigs/` folder for other definitions. To build additional toolchains we only have to clean our current toolchain project, removing the current `selected_ptxconfig` link and creating a new one.

```
$ ptxdist clean
$ rm selected_ptxconfig
$ ptxdist select ptxconfigs/any_other_toolchain_def.ptxconfig
$ ptxdist go
```

All toolchains will be installed side by side architecture dependent into directory

`/opt/OSELAS.Toolchain-2011.03/architecture_part.`

Different toolchains for the same architecture will be installed side by side version dependent into directory

`/opt/OSELAS.Toolchain-2011.03/architecture_part/version_part.`

3 Building a root filesystem for the mini2440

3.1 Extracting the Board Support Package

In order to work with a PTXdist based project we have to extract the archive first.

```
$ tar -zxf OSELAS.BSP-Pengutronix-Mini2440-2011.05.0.tar.gz
$ cd OSELAS.BSP-Pengutronix-Mini2440-2011.05.0
```

PTXdist is project centric, so now after changing into the new directory we have access to all valid components.

```
total 12
-rw-r--r-- 1 jb user 374 May 7 22:08 Changelog
-rw-r--r-- 1 jb user 2330 May 7 22:08 FAQ
-rw-r--r-- 1 jb user 177 May 7 22:08 README
drwxr-xr-x 3 jb user 128 May 7 22:08 configs
drwxr-xr-x 2 jb user 112 May 7 22:20 documentation
```

Notes about some of the files and directories listed above:

ChangeLog Here you can read what has changed in this release. Note: This file does not always exist.

documentation If this BSP is one of our OSELAS BSPs, this directory contains the Quickstart you are currently reading in.

configs A multiplatform BSP contains configurations for more than one target. This directory contains the platform configuration files.

projectroot Contains files and configuration for the target's runtime. A running GNU/Linux system uses many text files for runtime configuration. Most of the time the generic files from the PTXdist installation will fit the needs. But if not, customized files are located in this directory.

rules If something special is required to build the BSP for the target it is intended for, then this directory contains these additional rules.

patches If some special patches are required to build the BSP for this target, then this directory contains these patches on a per package basis.

tests Contains test scripts for automated target setup.

3.2 Feature Dependend Configurations

The FriendlyARM mini2440 comes in various incarnations. Mostly they differ in the NAND memory size, but also other features may present or not. Read the following sub sections to adapt this board support package to meet exactly your mini2440.

Note: In this documentation the FriendlyARM mini2440 with 64 MiB of NAND memory is referred only. But everything mentioned herein is also valid for mini2440 shipped with more than 64 MiB of NAND.

3.2.1 Identify Your Mini2440

The FriendlyARM mini2440s are shipped with various NAND memory sizes. The smallest one seems to be the 64 MiB one, the largest one comes with 1 GiB of NAND memory.

As this kind of memory needs some special treatment depending on its internal layout, we must distinguish them prior generating some images. This board support package comes with two configurations:

- `platformconfig-NAND-64M` for the mini2440 with 64 MiB of NAND memory
 - the NAND device is marked with the text K9F1208
- `platformconfig-NAND-128M` for the mini2440 with 128 MiB of NAND memory or more
 - these NAND devices are marked with the text K9F1G08, K9F2G08 or K9F8G08.

This is important while the platform selection step in section 3.4. As this section mentions the 64 MiB NAND configuration only (`platformconfig-NAND-64M`), we must select the 128 MiB configuration (`platformconfig-NAND-128M`) instead, if we are using a NAND memory larger than 64 MiB.



Running a 64 MiB configuration on a 128 MiB (or above) mini2440 will give us many confusing error messages (the same the other way around).

What differs in both configurations:

- erase block size (16 kiB versus 128 kiB)
- JFFS2 root filesystem creation (needs different parameters)
- count of spare blocks (important for NAND memory usage)
- partition sizes (due to different spare block counts)

3.2.2 Network Adaptions

Adaptions to the local network can be made for the runtime of the bootloader or Linux based system.

Bootloader Barebox

As there is no generic network setting available, some adaptions to our own network should be done prior building the board support package.

To do so, we should open one of the following files with our favourite editor:

- `configs/mini2440-master/barebox-64m-env/config` if we are using a mini2440 with 64 MiB NAND
- `configs/mini2440-master/barebox-128m-env/config` if we are using a mini2440 with 128 MiB or larger NAND

These settings are relevant only for the bootloader. The file content will be the default settings later on, when we are using the mini2440. Default settings mean they can be permanently changed at runtime later on. But whenever the bootloader loses its environment it will fall back to the settings in this file. So, to avoid more changes at runtime than required, we should do the settings carefully here.

We can keep the `ip=dhcp` option enabled. This requires a DHCP server in the network to be able to update the NAND memory content or to use NFS root filesystem while development. But also in this case at least the `eth0.ethaddr` must be set, to give our network device an unique MAC address.

If no DHCP server is available, a static network setting can be used instead. We just comment out the `ip=dhcp` option and enable all the `eth0.*` lines and give them usefull values.

If we want to use an NFS based root filesystem, we also should adapt the `nfsroot` setting.



Don't forget the setting of an unique MAC address. At least the entry `eth0.ethaddr` must be set.

Linux Kernel

To define network settings used at the runtime of the Linux kernel, we must adapt the file `projectroot/etc/network/interfaces` instead.

3.2.3 Feature Adaptions

Other features of the mini2440 are:

- the connected LC display
- if a touch is present
- if a camera is present

These features can be enabled/disabled or configured at runtime with the kernel parameter `mini2440=`. The content of this parameter is also configured in the `config` files mentioned above.

The most interesting part is the connected LC display. It must be configured correctly to make it work at runtime. Here a list of currently known LC displays:

- 3.5" TFT + touchscreen (LCDN3502/NL2432HC22-23B)
- 7" TFT + touchscreen
- VGA shield
- 3.5" TFT + touchscreen (T35)
- 5.6" TFT + touchscreen (AT056TN52)
- 3.5" TFT + touchscreen (X35)

The list above corresponds to the number (beginning with 0) given to the `mini2440=` kernel parameter to define the LC display in use.

After these adaptations are made, we can continue building the board support package.

When starting the kernel later on, it will output the list of supported displays with the currentlty selected one embraced.

```
MINI2440: LCD [0:240x320] 1:800x480 2:1024x768 3:240x320 4:640x480 5:240x320
```


3.3 Selecting a Userland Configuration

First of all we have to select a userland configuration. This step defines what kind of applications will be built for the hardware platform. The OSELAS.BSP-Pengutronix-Mini2440-2011.05.0 comes with a predefined configuration we select in the following step:

```
$ ptxdist select configs/ptxconfig
info: selected ptxconfig:
      'configs/ptxconfig'
```

3.4 Selecting a Hardware Platform

Before we can build this BSP, we need to select one of the possible platforms to build for. In this case we want to build for the mini2440:

```
$ ptxdist platform configs/platform-friendlyarm-mini2440/platformconfig-NAND-64M
info: selected platformconfig:
      'configs/platform-friendlyarm-mini2440/platformconfig-NAND-64M'
```

Note: If you have installed the OSELAS.Toolchain() at its default location, PTXdist should already have detected the proper toolchain while selecting the platform. In this case it will output:

```
found and using toolchain:
' /opt/OSELAS.Toolchain-2011.03/arm-v4t-linux-gnueabi/┐
  gcc-4.5.2-glibc-2.13-binutils-2.21-kernel-2.6.36-sanitized/bin'
```

If it fails you can continue to select the toolchain manually as mentioned in the next section. If this autodetection was successful, we can omit the steps of the section and continue to build the BSP.

3.5 Selecting a Toolchain

If not automatically detected, the last step in selecting various configurations is to select the toolchain to be used to build everything for the target.

```
$ ptxdist toolchain /opt/OSELAS.Toolchain-2011.03/arm-v4t-linux-gnueabi/┐
  gcc-4.5.2-glibc-2.13-binutils-2.21-kernel-2.6.36-sanitized/bin
```

3.6 Building the Root Filesystem

Now everything is prepared for PTXdist to compile the BSP. Starting the engines is simply done with:

```
$ ptxdist go
```

PTXdist does now automatically find out from the `selected_ptxconfig` and `selected_platformconfig` files which packages belong to the project and starts compiling their *targetinstall* stages (that one that actually puts the compiled binaries into the root filesystem). While doing this, PTXdist finds out about all the dependencies between the packets and brings them into the correct order.

While the command `ptxdist go` is running we can watch it building all the different stages of a packet. In the end the final root filesystem for the target board can be found in the `platform-mini2440/root/` directory and a bunch of `*.ipk` packets in the `platform-mini2440/packages/` directory, containing the single applications the root filesystem consists of.

3.7 Building an Image

After we have built a root filesystem, we can make an image, which can be flashed to the target device. To do this call

```
$ ptxdist images
```

PTXdist will then extract the content of priorly created `*.ipk` packages to a temporary directory and generate an image out of it. PTXdist supports following image types:

- **hd.img:** contains grub bootloader, kernel and root files in a ext2 partition. Mostly used for X86 target systems.
- **root.jffs2:** root files inside a jffs2 filesystem.
- **uRamdisk:** a u-boot loadable Ramdisk
- **initrd.gz:** a traditional initrd RAM disk to be used as `initrdramfs` by the kernel
- **root.ext2:** root files inside a ext2 filesystem.
- **root.squashfs:** root files inside a squashfs filesystem.
- **root.tgz:** root files inside a plain gzip compressed tar ball.
- **root.ubi:** root files inside a ubi volume.

The to be generated image types and additional options can be defined with

```
$ ptxdist platformconfig
```

Then select the submenu “image creation options”. The generated image will be placed into `platform-mini2440/images/`.



Only the content of the `*.ipk` packages will be used to generate the image. This means that files which are put manually into the `platform-mini2440/root/` will not be enclosed in the image. If custom files are needed for the target, install it with PTXdist.

3.7.1 Deploying the mini2440

After building all relevant images, in the next step we must bring in the new software into our target.

What we need for this step to do:

- a working network infrastructure
- host with network and USB capabilities
- a working TFTP server on our host

- some cables
 - network
 - USB-A to USB-B
 - RS232
- serial terminal running on our host

We assume here:

- the directory of the TFTP server is /tftpboot
- network is already configured for the host and the target (refer section 3.2.2)
- all connections are done (network, USB, serial)
- the serial terminal is able to handle 8 bits at 115200 Bd

To bring in the new software we start to bring in the new bootlader. This is the trickiest part, as we need special tools on our host and the target and we have to deal with confusing error messages.

First of all, we must change the **S2** switch on our mini2440 into the **NOR** position to start the internal **vivi** boot-loader. After switching on the mini2440, the vivi bootloader will greet us with:

```
##### FriendlyARM BIOS for 2440 #####
[x] bon part 0 320k 2368k
[v] Download vivi
[k] Download linux kernel
[y] Download root_yaffs image
[a] Absolute User Application
[n] Download Nboot
[l] Download WinCE boot-logo
[w] Download WinCE NK.bin
[d] Download & Run
[z] Download zImage into RAM
[g] Boot linux from RAM
[f] Format the nand flash
[b] Boot the system
[s] Set the boot parameters
[u] Backup NAND Flash to HOST through USB(upload)
[r] Restore NAND Flash from HOST through USB
[q] Goto shell of vivi
[i] Version: 1026-12
Enter your selection:
```

We want to use the **vivi** bootloader to bring in the new barebox bootloader into mini2440's RAM. In order to do so, we need the size in bytes of barebox's binary:

```
$ ls -l platform-mini2440/images/barebox-image
-rw-r--r-- 1 jb user 147844 Apr 27 14:00 platform-mini2440/images/barebox-image
```

The size of this binary may differ in your case. In our case here it is **147844**.

With this size we instruct the **vivi** bootloader to expect this amount of bytes from the USB and store it to the internal RAM. To do so, we enter '**q**' to enter vivi's shell. Then we start the download command.

```
Enter your selection: q
Supervivi> load ram 0x31000000 147844 u
```

Please consider the 147844 number here. This number must be adapted to your own size of barebox's image size.

At this point of time many error messages can happen. The mini2440 may output USB host is not connected yet. In this case disconnect the USB cable again, powercycle the mini2440 and try again.

At the host side the system may not be able to enumerate the mini2440 correctly. In this case also disconnect the mini2440, powercycle it and connect it again.

If the host was able to enumerate the mini2440 successfully can be checked with the `lsusb` command. If the following line occurs in the list, the mini2440 is successfully enumerated:

```
Bus 001 Device 023: ID 5345:1234 Owon PDS6062T Oscilloscope
```

Note: The bus and device number may differ in your case.

If the USB connection is up and working on both sides, we can start to push the new bootloader into the target. This BSP comes with the required tool to do so.

```
$ sudo platform-mini2440/sysroot-host/bin/usbpush platform-mini2440/images/barebox-image
```

When the transfer was successful, the `usbpush host` tool will output:

```
csum = 0x74f9
send_file: addr = 0x30000000, len = 0x0024124
```

At the target side we will see:

```
Now, Downloading [ADDRESS:3100000h,TOTAL:147854]
RECEIVED FILE SIZE: 147854 (144KB/S, 1S)
Downloaded file at 0x3100000, size = 147844 bytes
```

Note: The numbers shown may differ from the number you will see.

After the transfer was successful, we can now run the downloaded bootloader:

```
Supervivi> go 0x31000000
go to 0x31000000
argument 0 = 0x00000000
argument 1 = 0x00000000
argument 2 = 0x00000000
argument 3 = 0x00000000
```

This will start the barebox bootloader on the mini2440 that will greet us with:

```
barebox 2011.05.0-mini2440-ptx-2011.05.0 (May 7 2011 - 14:00:29)

Board: Mini 2440
NAND device: Manufacturer ID: 0xec, Chip ID: 0x76 (Samsung NAND 64MiB 3,3V 8-bit)
Bad block table found at page 131040, version 0x01
Bad block table found at page 131008, version 0x01
dm9000 i/o: 0x20000300, id: 0x9000a46
eth@eth0: got MAC address from EEPROM: FF:FF:FF:FF:FF:FF
refclk: 12000 kHz
mpll: 405000 kHz
upll: 48000 kHz
fclk: 405000 kHz
hclk: 101250 kHz
```

```
pclk:      50625 kHz
SDRAM1:    CL4@101MHz
SDRAM2:    CL4@101MHz
Malloc space: 0x33a00000 -> 0x33e00000 (size 4 MB)
Stack space : 0x339f8000 -> 0x33a00000 (size 32 kB)
envfs: wrong magic on /dev/env0
no valid environment found on /dev/env0. Using default environment
running /env/bin/init...

Hit any key to stop autoboot: 3
```

We can stop the autoboot timeout by hitting any key. We are now in the shell environment of barebox. To update the NAND content in the next step we need a working network first. One check is to show the current setting of the network interface. You should see your own settings here, done in section 3.2.2. Here an example for a static network configuration:

```
mini2440:/ devinfo eth0
base : 0x00000000
size : 0x00000000
driver: none

Parameters:
    ipaddr = 192.168.1.240
    ethaddr = 00:04:f3:00:06:35
    gateway = 192.168.1.2
    netmask = 255.255.255.0
    serverip = 192.168.1.7
```

If you do not see reasonable values here and you are using the DHCP option, run the `dhcp` command first:

```
mini2440:/ dhcp
DHCP client bound to address 192.168.1.27
```

If you do not use DHCP for network configuration, you must edit the file `/env/config` first.

```
mini2440:/ edit /env/config
```

Note: Barebox supports auto completion of commands, paths and filenames. Use the well known TAB key.

Edit the lines beginning with `eth0.*` and give them reasonable values. We can leave the editor by hitting CTRL-D to save our changes, or CTRL-C to discard any change. If we want these new settings to be persistent, we can save them now to the NAND:

```
mini2440:/ saveenv
```

To make the new static network configuration work, we must source the `config` file again:

```
mini2440:/ . /env/config
```

Running the `devinfo eth0` command again should now show reasonable values for the network interface. If it really works can be tested by pinging other hosts:

```
mini2440:/ ping 192.168.1.7
host 192.168.1.7 is alive
```

In order to store all relevant components into the NAND, we can use some builtin features of barebox right now. First we must copy at the host side all generated images from the board support package to the directory used by the TFTP server:

```
$ cp platform-mini2440/images/barebox-image /tftpboot/barebox-mini2440
$ cp platform-mini2440/images/root.jffs2 /tftpboot/root-mini2440.jffs2
$ cp platform-mini2440/images/linuximage /tftpboot/uImage-mini2440
```

Then we can run a script at the mini2440's side:

```
mini2440:/ update -t barebox -d nand
mini2440:/ update -t rootfs -d nand
mini2440:/ update -t kernel -d nand
```

That's all. To boot with the new firmware, we must change the **S2** switch now back into the **NAND** position. Powercycle the mini2440 or press its reset button and the new software will start.

4 Special Notes

4.1 Framebuffer

This driver gains access to the display via device node `/dev/fb0`. For this BSP the LCDN3502-23B display with a resolution of 240x320 is supported.

A simple test of this feature can be run with:

```
# fbtest
```

This will show various pictures on the display.

You can check your framebuffer resolution with the command

```
# fbset
```

NOTE: `fbset` cannot be used to change display resolution or color depth. Depending on the framebuffer device different kernel command line are mostly needed to do this. Please refer to the manual of your display driver for more details.

4.2 GPIO

Like most modern System-on-Chip CPUs, the S3C2440 has numerous GPIO pins. Some of them are inaccessible for the userspace as Linux drivers use them internally. Others are also used by drivers but are exposed to userspace via `sysfs`. Finally, the remaining GPIOs can be requested for custom use by userspace, also via `sysfs`.

Refer to the in-kernel documentation `Documentation/gpio.txt` for complete details how to use the `sysfs`-interface for manually exporting GPIOs.

4.2.1 GPIO Usage Example

When generic architecture GPIO support is enabled in the kernel, some new entries are appearing in `sysfs`. Everything is controlled via read- and writable files to generate events on the digital lines.

We find all control files in `/sys/class/gpio`. In that path, there are a number of `gpiochipXXX` entries, with XXX being a decimal number. Each of those folders provides information about a single gpio controller registered on the mini2440 board, for example with `gpiochip192`:

```
# ls /sys/class/gpio/gpiochip192
base      label      ngpio      power      subsystem  uevent
```

The entry `base` contains information about the base GPIO number and `ngpio` contains the whole amount of GPIOs provided by this GPIO controller.

We use `GPIO193` as an example to show the usage of single GPIOs.

```
# echo 193 > /sys/class/gpio/export
```

This way we export `gpio193` for userspace usage. If the export was succesful, we will find a new directory named `/sys/class/gpio/gpio193` afterwards. Within this directory we will be able to find the entries to access the functions of this gpio. If we wish to set the direction and initial level of the GPIO, we can use the command:

```
# echo high > /sys/class/gpio193/direction
```

This way we export `GPIO193` for userspace usage and define our GPIO's direction attribute to an output with initially high level. We can change the value or direction of this GPIO by using the entries `direction` or `value`.

Note: This method is not very fast, so for quickly changing GPIOs it is still necessary to write a kernel driver. The shown method works fine for example to influence an LED directly from userspace.

To unexport an already exported GPIO, write the corresponding gpio-number into `/sys/class/gpio/export`.

```
# echo 193 > /sys/class/gpio/unexport
```

Now the directory `/sys/class/gpio/gpio193` will disapear.

Note: The GPIO193 is available at connector 4, pin 17 for measurement.

4.3 I²C Master

The S3C2440 processor based mini2440 supports a dedicated I²C controller onchip. The kernel supports this controller as a master controller.

Additional I²C device drivers can use the standard I²C device API to gain access to their devices through this master controller. For further information about the I²C framework see [Documentation/i2c](#) in the kernel source tree.

4.3.1 I²C Device AT24c08

This device is a 1024 bytes non-volatile memory for general purpose usage.

This type of memory is accessible through the sysfs filesystem. To read the EEPROM content simply `open()` the entry `/sys/bus/i2c/devices/0-0050/eeprom` and use `fseek()` and `read()` to get the values.

4.4 LEDs

The LEDs on the mini2440 can be controlled via the LED-subsystem of the Linux kernel. It provides methods for switching them on and off as well as using so-called triggers, which trigger the LED according to f.e. a simple timer. That enables us to make it blink with any frequency we want.

All LEDs can be found in the directory `/sys/class/leds`. Each one has its own subdirectory. We will use `led1` for the following examples.

Per directory, you have a file named `brightness` which can be read and written with a decimal value between 0 and 255. The first one means LED off, the latter maximum brightness. Inbetween values scale the brightness if the LED supports that. If not, non-zero means just LED on.


```
/sys/class/leds/led1# echo 255 > brightness; # LED on
/sys/class/leds/led1# echo 128 > brightness; # LED at 50% (if supported)
/sys/class/leds/led1# echo 0 > brightness; # LED off
```

LEDs can be connected to triggers. A list of available triggers we can get from the `trigger` entry

```
/sys/class/leds/led1# cat trigger
[none] nand-disk mmc0 timer backlight
```

The embraced entry is the currently connected trigger to this LED.

To change the trigger source to the *timer*, just run a:

```
/sys/class/leds/led1# echo timer > trigger
```

If the timer-trigger is activated you should see two additional files in the current directory, namely `delay_on` and `delay_off`. You can read and write decimal values there, which will set the corresponding delay in milliseconds. As an example:

```
/sys/class/leds/led1# echo 250 > delay_on
/sys/class/leds/led1# echo 750 > delay_off
```

will blink the LED being on for 250ms and off for 750 ms.

Replace `timer` with `none` to disable the trigger again. Or select a different one from the list read from the `trigger` entry.

Refer to `Documentation/leds-class.txt` in-kernel documentation for further details about this subsystem.

4.5 MMC/SD Card

The mini2440 supports *Secure Digital Cards* and *Multi Media Cards* in conjunction with its SD card connector to be used as general purpose blockdevices. These devices can be used in the same way as any other blockdevice.



These kind of devices are hot pluggable, so you must pay attention not to unplug the device while its still mounted. This may result in data loss.

After inserting an MMC/SD card, the kernel will generate new device nodes in `dev/`. The full device can be reached via its `/dev/mmcblk0` device node, MMC/SD card partitions will occur in the following way:

```
/dev/mmcblk0pY
```

`Y` counts as the partition number starting from 1 to the max count of partitions on this device.

Note: These partition device nodes will only occur if the card contains a valid partition table ("harddisk" like handling). If it does not contain one, the whole device can be used for a filesystem ("floppy" like handling). In this case `/dev/mmcblk0` must be used for formatting and mounting.

The partitions can be formatted with any kind of filesystem and also handled in a standard manner, e.g. the `mount` and `umount` command work as expected.

4.6 Network

The mini2440 module has a DM9000 ethernet chip onboard, which is being used to provide the `eth0` network interface. The interface offers a standard Linux network port which can be programmed using the BSD socket interface.

4.7 SPI Master

The mini2440 board supports an SPI bus, based on the S3C2440's integrated SPI controller. It is connected to the onboard devices using the standard kernel method, so all methods described here are not special to the mini2440.

Connected devices can be found in the `sysfs` at the path `/sys/bus/spi/devices`. It depends on the corresponding SPI slave device driver if it provides access to the SPI slave device through this way (`sysfs`), or any different kind of API.



Currently no SPI slave devices are registered, so the `/sys/bus/spi/devices` directory is empty.

4.8 Touch

A simple test of this feature can be run with:

```
# ts_calibrate
```

to calibrate the touch and with:

```
# ts_test
```

to run a simple application using this feature.

To see the exact events the touch generates, we can also use the `evtest` tool.

```
# evtest /dev/input/event1
Input driver version is 1.0.1
Input device ID: bus 0x19 vendor 0xdead product 0xbeef version 0x102
Input device name: "S3C24XX TouchScreen"
Supported events:
  Event type 0 (Sync)
  Event type 1 (Key)
    Event code 330 (Touch)
  Event type 3 (Absolute)
    Event code 0 (X)
      Value      0
      Min        0
      Max       1023
    Event code 1 (Y)
      Value      0
      Min        0
      Max       1023
Testing ... (interrupt to exit)
```

Whenever we touch the screen this tool lists the values the driver reports.

4.9 USB Host Controller Unit

The mini2440 supports a standard OHCI Rev. 1.0a compliant host controller onboard for low and full speed connections. Up to two ports are supported by this CPU.

4.10 Watchdog

The internal watchdog will be activated when an application opens the device `/dev/watchdog`. Default timeout is 15 seconds. An application must periodically write to this device. It does not matter what is written. Just the interval between these writes should not exceed the timeout value, otherwise the system will be reset.

For testing the hardware, there is also a shell command which can do the triggering:

```
# watchdog -t <trigger-time-in-seconds> /dev/watchdog
```

This command is part of the busybox shell environment. Keep in mind, that it should only be used for testing. If the watchdog gets fed by it, a crash of the real application will go unnoticed.

For the mini2440 the default 60 seconds interval period the tool is using is too long. The driver for the S3C2440 only supports up to a 40 seconds interval. So, the additional parameter `-T 40` must be given.

4.11 Get the latest BSP Release for the mini2440

Information and the latest release of the mini2440 BSP you can find at our website at

http://www.oselas.org/oselas/bsp/index_en.html

4.12 Be Part of the mini2440 BSP Development

If you want to use the latest and greatest board support package for the mini2440 you can use the git repository as your working source instead of a release archive.

The git repository can be found here:

```
ssh://gitolite@git-public.pengutronix.de:/OSELAS.BSP-Pengutronix-Mini2440.git
```

If you want to contribute to this project by sending patches, these patches should always be based on the **master** branch of this repository.

5 Document Revisions

2011/05/07	Initial Revision
x	

6 Getting help

Below is a list of locations where you can get help in case of trouble. For questions how to do something special with PTXdist or general questions about Linux in the embedded world, try these.

6.1 Mailing Lists

6.1.1 About PTXdist in Particular

This is an English language public mailing list for questions about PTXdist. See

http://www.pengutronix.de/maillinglists/index_en.html

how to subscribe to this list. If you want to search through the mailing list archive, visit

<http://www.mail-archive.com/>

and search for the list *ptxdist*. Please note again that this mailing list is just related to the PTXdist as a software. For questions regarding your specific BSP, see the following items.

6.1.2 About Embedded Linux in General

This is a German language public mailing list for general questions about Linux in embedded environments. See

http://www.pengutronix.de/maillinglists/index_de.html

how to subscribe to this list. Note: You can also send mails in English.

6.2 News Groups

6.2.1 About Linux in Embedded Environments

This is an English newsgroup for general questions about Linux in embedded environments.

comp.os.linux.embedded

6.2.2 About General Unix/Linux Questions

This is a German newsgroup for general questions about Unix/Linux programming.

de.comp.os.unix.programming

6.3 Chat/IRC

About PTXdist in particular

irc.freenode.net:6667

Create a connection to the **irc.freenode.net:6667** server and enter the chatroom **#ptxdist**. This is an English room to answer questions about PTXdist. Best time to meet somebody there is at European daytime.

6.4 FriendlyARM mini2440 specific Mailing List

oselas@community.pengutronix.de

This is a community mailing list open for everyone for all mini2440's board support package related questions. Refer our page at

http://www.pengutronix.de/maillinglists/index_en.html

to subscribe to this mailing list.

Note: Please be aware that we cannot answer hardware only related questions on this list.

6.5 Commercial Support

You can order immediate support through customer specific mailing lists, by telephone or also on site. Ask our sales representative for a price quotation for your special requirements.

Contact us at:

Pengutronix
Peiner Str. 6-8
31137 Hildesheim
Germany
Phone: +49 - 51 21 / 20 69 17 - 0
Fax: +49 - 51 21 / 20 69 17 - 55 55

or by electronic mail:

sales@pengutronix.de

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**Pengutronix e.K.
Peiner Str. 6-8
31137 Hildesheim
Germany**

**Phone: +49 - 51 21 / 20 69 17 - 0
Fax: +49 - 51 21 / 20 69 17 - 55 55**

